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August 22, 2003

**Via Facsimile 703.872.9318**Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Re: Serial No.: 09/738,193  
Art Unit: 2827  
Examiner: David E. Graybill  
System, Method and Apparatus for Constructing a Semiconductor Wafer-Interposer Using B-Stage Laminates  
Our File: 1384-1023

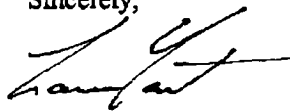
Dear Sir:

Enclosed for filing please find the following items relating (17 pages) to the above-identified application:

- (1) Response Under 37 C.F.R. §1.111 (15 pages);
- (2) Petition Under 37 C.F.R. §1.136 for a One Month Extension of Time (1 page); and
- (3) Form PTO-2038 authorizing a payment in the amount of \$55.00 (1 page).

If you have any questions or comments concerning this matter, please call the undersigned at your earliest convenience. Otherwise, please accept the enclosed.

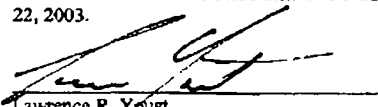
Sincerely,

Lawrence R. Youst  
Reg. No. 38,795

Enclosures

## Certificate of Transmission Under 37 C.F.R. §1.8

I hereby certify that this correspondence is being transmitted by facsimile to the United States Patent and Trademark Office on August 22, 2003.

  
Lawrence R. Youst*Serving the Spirit of Innovation®*

Attorney Docket No.: 1384-1023

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application: John L. Pierce

Serial No.: 09/738,193

Filed: December 15, 2000

Art Unit: 2827

Examiner: David E. Graybill

For: System, Method and Apparatus for Constructing  
a Semiconductor Wafer-Interposer Using B-  
Stage Laminates

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**FAX RECEIVED****AUG 25 2003****GROUP 2800****OFFICIAL****Response Under 37 C.F.R. §1.111**

Dear Sir:

Further to our telephone interview of August 21, 2003, this is a response to the Office Action dated July 16, 2003, which was due for reply by August 16, 2003. A petition and filing fee for an extension of time for a total period of one month is filed herewith. This Response is thereby timely filed on or before September 16, 2003.